



REVISIONS				
REV	DESCRIPTION	E.C.N.	DATE	BY/APP'D
1	SEE SHEET 1			

PACKAGE DIMENSIONS					
X1 DESIGNATOR	X1 PACKAGE WIDTH $\pm 30\mu\text{m}$	X2 DESIGNATOR	X2 PACKAGE LENGTH $\pm 30\mu\text{m}$	X3 DESIGNATOR	X3 PACKAGE HEIGHT $\pm 75\mu\text{m}$
Z	2441	Z	2441	A	600
1	2466	1	2466		
A	2492	A	2492		
B	2517	B	2517		
C	2543	C	2543		
D	2568	D	2568		
E	2593	E	2593		
F	2619	F	2619		
G	2644	G	2644		
H	2670	H	2670		
J	2695	J	2695		
K	2720	K	2720		
L	2746	L	2746		
M	2771	M	2771		
N	2797	N	2797		
P	2822	P	2822		
Q	2847	Q	2847		
R	2873	R	2873		
S	2898	S	2898		
T	2924	T	2924		
U	2949	U	2949		

APPROVALS		DATE	 National Semiconductor <small>2900 Semiconductor Dr., Santa Clara, CA 95052-8090</small>		
DRAWN	MARTA SUCHY	03/22/2002			
DFTG. CHK.	THANH LEQUANG	12/10/2003			
ENGR. CHK.	HAU NGUYEN	12/10/2003			
PROJECTION		SCALE	SIZE	DRAWING NUMBER	REV
 MM		NTS	B	(SC)MKT-TLA25XXX	D
FORMERLY: N/A		SHEET 2 of 2			