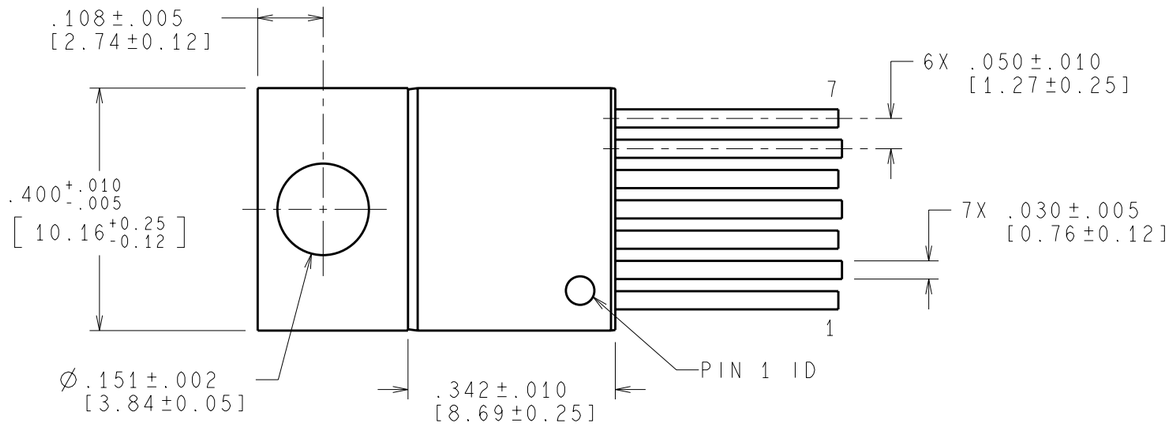
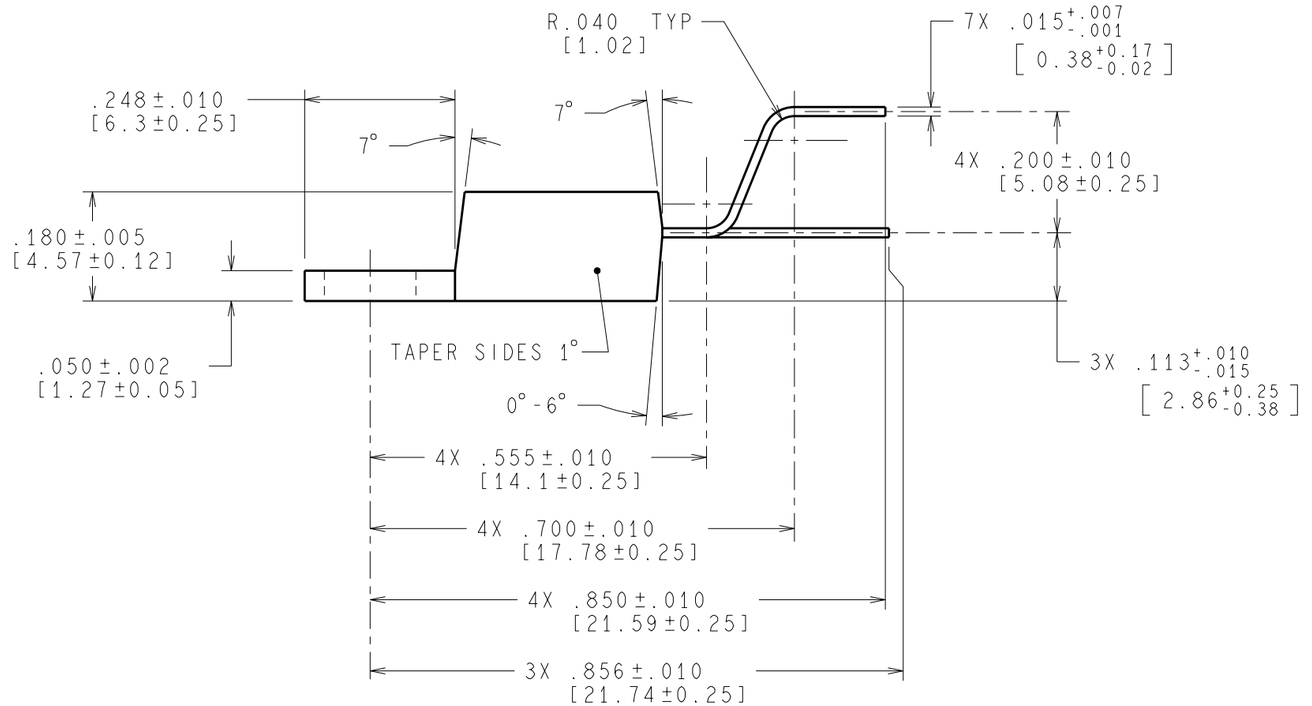
REVISIONS				
REV	DESCRIPTION	E.C.N.	DATE	BY/APP'D
C	REDRAW ON ProE; STRAIGHT LD LENGTH .856±.010 WAS .880-.900	12166	04/19/1999	NR/TL/NR
D	UPDATE DWG & TITLE PER CURRENT STD; REVISE NOTE 1, REMOVE NOTE 2 & UPDATE NOTE 3; CHANG DWG FORMAT TO B SIZE.	1025	07/15/2003	MS/RW
E	TITLE: 7 LD,.050 in PITCH WAS 7 LD,0.50 in PITCH	1143	07/29/2003	MS/RW





CONTROLING DIMENSIONS IS INCH
VALUES IN [ ] ARE MILLIMETERS

NOTES: UNLESS OTHERWISE SPECIFIED

1. FOR LEAD FINISH THICKNESS AND COMPOSITION, SEE "SOLDER INFORMATION": IN THE PACKAGING SECTION OF THE NATIONAL SEMICONDUCTOR WEB PAGE (www.national.com).

2. NO JEDEC REGISTRATION AS OF JULY 2003.

APPROVALS	DATE
DRAWN NOR A.R	04/19/1999
DFTG. CHK. THANH LEQUANG	07/29/2003
ENGR. CHK. RANDALL WALBERG	07/29/2003

PROJECTION INCH [MM]

National Semiconductor
2900 Semiconductor Dr., Santa Clara, CA 95052-8090

TO-220,MOLDED,
.400x.590x.180in BODY,
7 LD,.050in PITCH,STAGGERED

SCALE	SIZE	DRAWING NUMBER	REV
NTS	B	(SC)MKT-TA07B	E

FORMERLY: N/A

SHEET 1 of 1