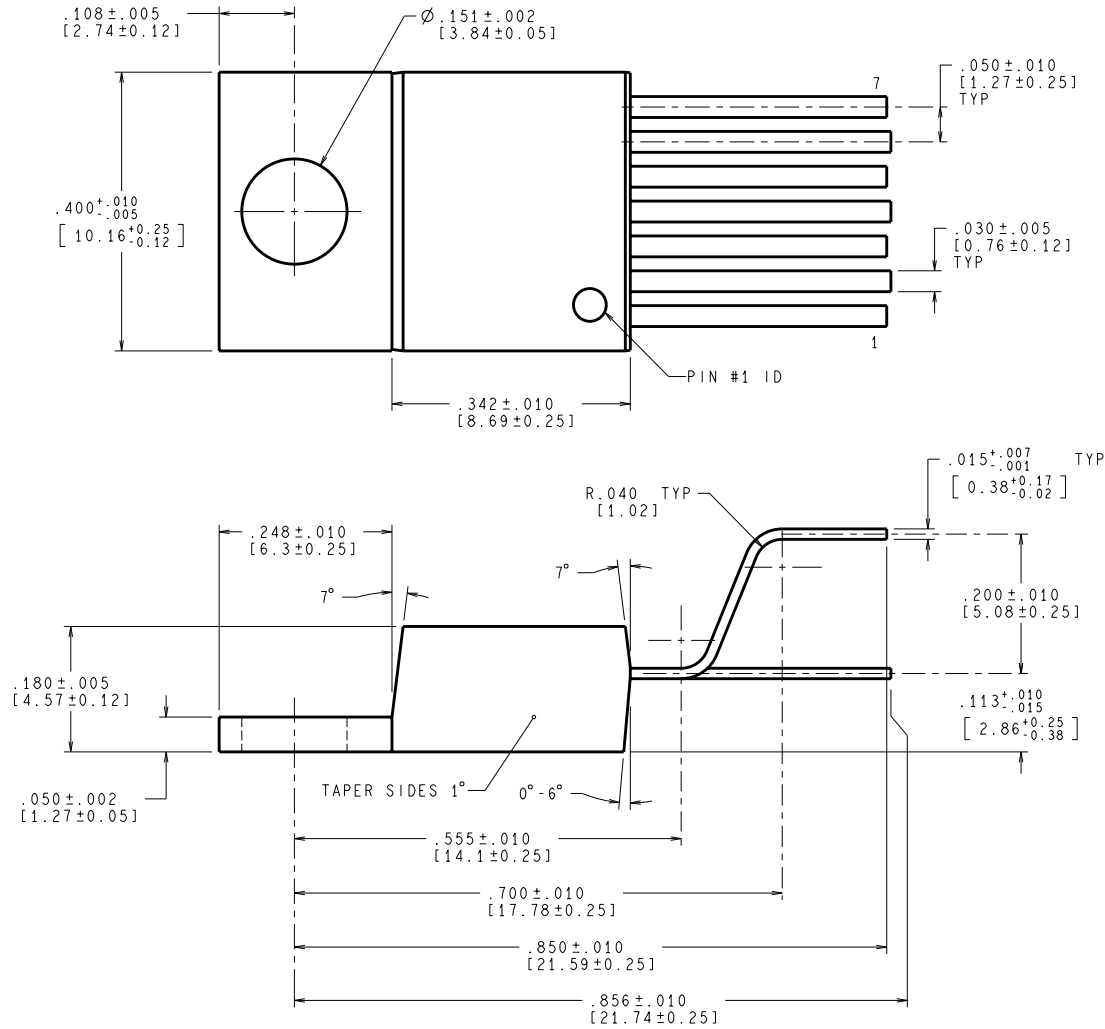


REVISIONS				
LTR	DESCRIPTION	E.C.N.	DATE	BY/APP'D
C	REDRAWN ON ProE; STRAIGHT LD LENGTH .856±.010 WAS .880-900	12166	04/19/1999	NR/TL/NR



CONTROLLING DIMENSIONS IS INCH
VALUES IN [] ARE MILLIMETERS

NOTES: UNLESS OTHERWISE SPECIFIED

- STANDARD LEAD FINISH:
200 MICROINCHES/5.08 MICROMETERS MINIMUM LEAD/TIN 15/85
ON OLIN 194 OR EQUIVALENT.
- BODY DIMENSIONS BASED ON JEDEC STANDARD, TO-220,
VARIATION AB, ISSUE J, DATED 3-24-87.
- NO JEDEC REGISTRATION AS OF APRIL 1999.

APPROVALS	DATE	National Semiconductor			
DRAWN NOR A.R	04/19/1999	2900 Semiconductor dr, Santa Clara, CA 95052-8090			
DFTG. CHK.		TO-220, MOLDED, 7 LEAD, STAGGERED			
ENGR. CHK. N. RAHMAN	04/19/1999				
		SCALE	SIZE	DRAWING NUMBER	REV
		N/A	C	(SC)MKT-TA07B	C
		DO NOT SCALE DRAWING SHEET 1 of 1			