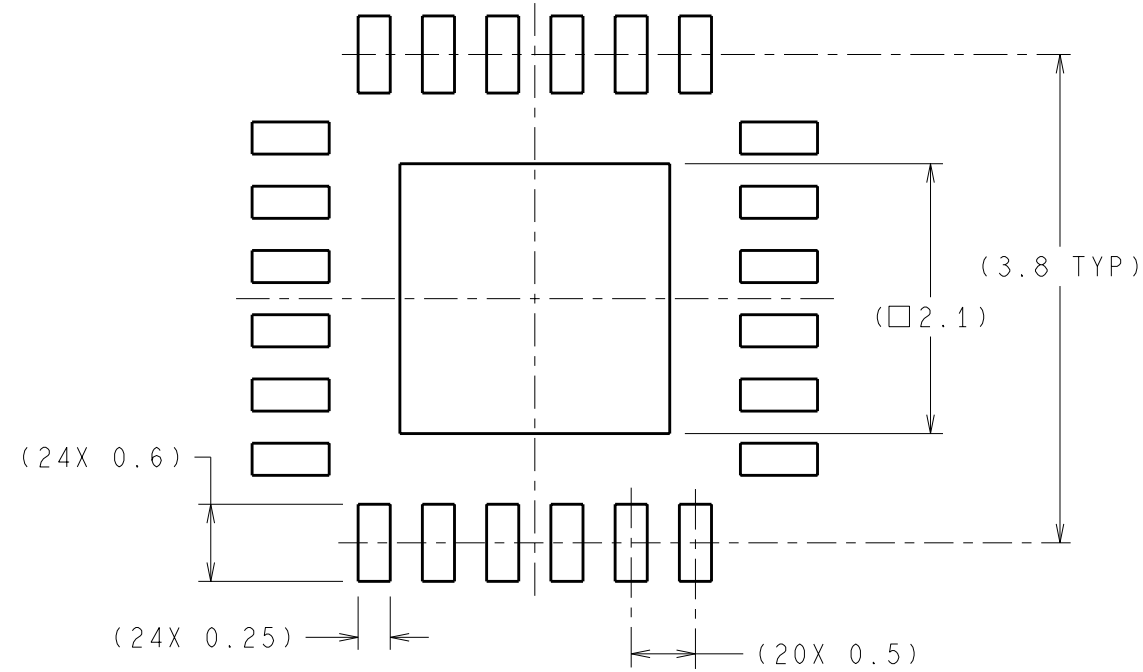
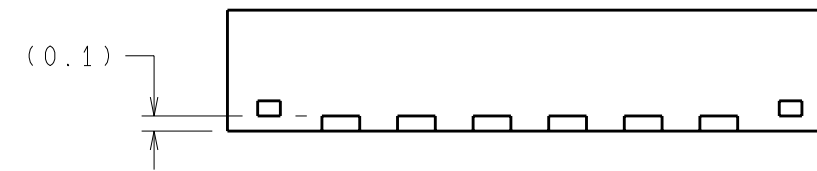


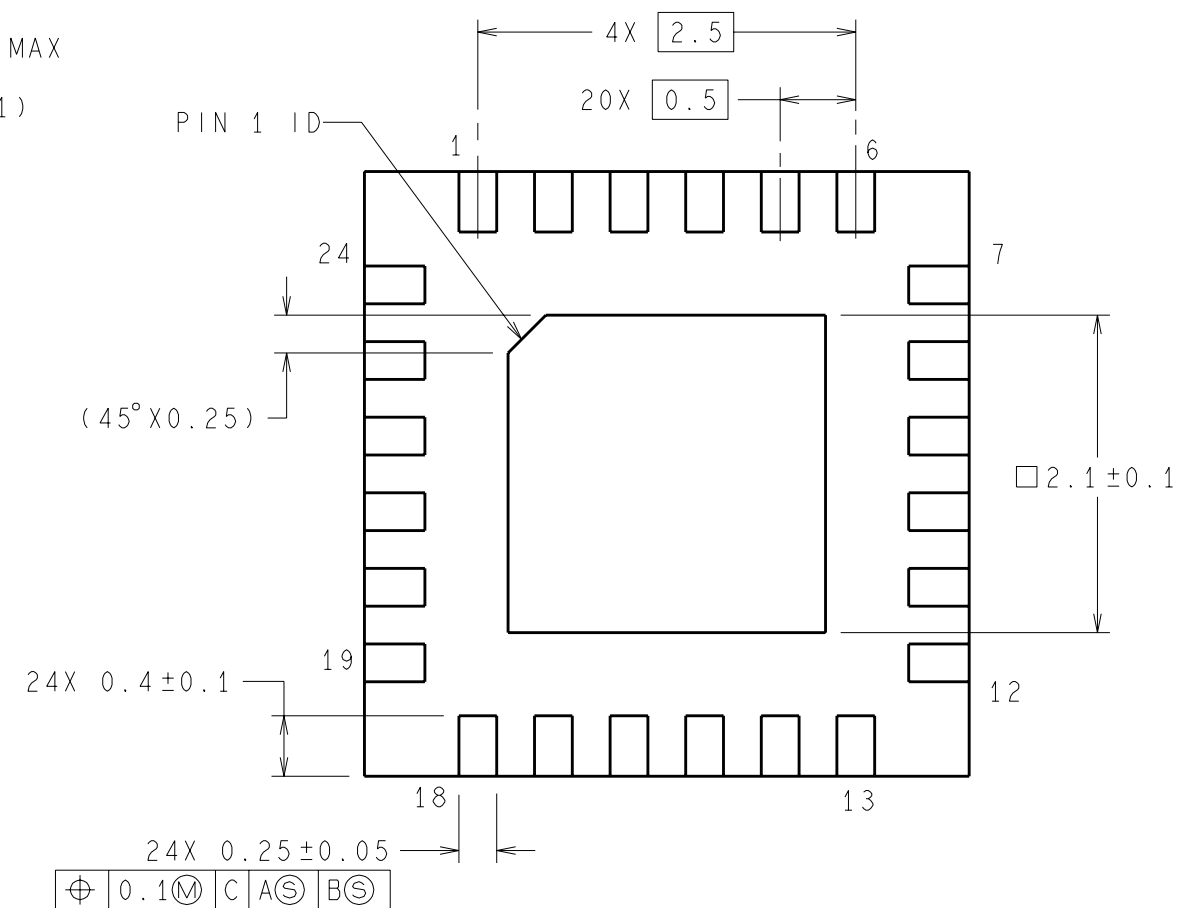
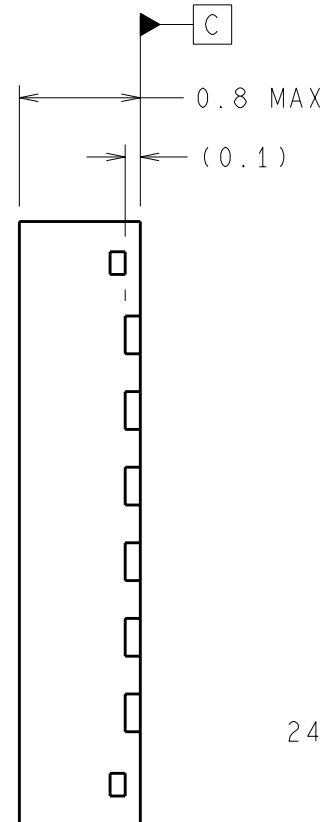
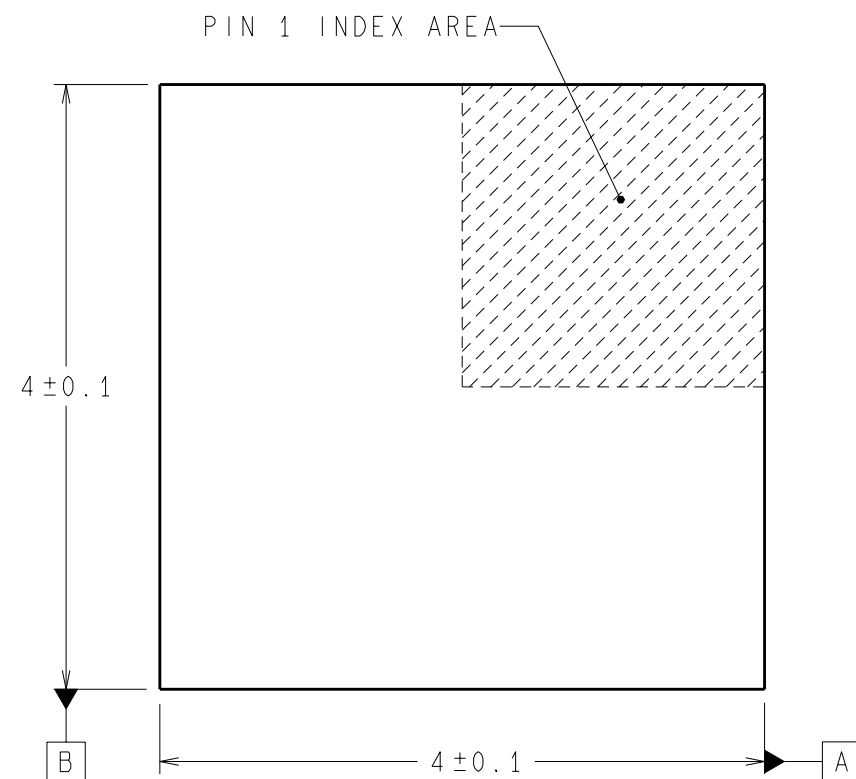
REVISIONS				
REV	DESCRIPTION	E.C.N.	DATE	BY/APP'D
A	RELEASE TO DOCUMENT CONTROL	1208	09/17/2003	AS/TL/SN



DIMENSIONS ARE IN MILLIMETERS
DIMENSION IN () FOR REFERENCE ONLY



RECOMMENDED LAND PATTERN



NOTES: UNLESS OTHERWISE SPECIFIED

- FOR SOLDER THICKNESS AND COMPOSITION, SEE "SOLDER INFORMATION" IN THE PACKAGING SECTION OF THE NATIONAL SEMICONDUCTOR WEB PAGE (www.national.com).
- REFERENCE JEDEC REGISTRATION MO-220, VARIATION WGGD-2.

APPROVALS		DATE	 National Semiconductor 2900 Semiconductor Dr., Santa Clara, CA 95052-8090		
DRAWN	ASNOR SULAIMAN	09/17/2003			
DFTG. CHK.	THANH LEQUANG	09/17/2003	LLP, PLASTIC, QUAD, 4x4x0.8mm BODY, 24 LD, 0.5mm PITCH, NO PULLBACK, CUSTOM		
ENGR. CHK.	N. SANTHIRAN	09/17/2003			
PROJECTION		SCALE	SIZE	DRAWING NUMBER	REV
 MM		NTS	B	(SC)MKT-SQA24C	A
FORMERLY: N/A		SHEET 1 of 1			