

1

2

3

4

5

A

V_{OC}V_{DD}

GND

R_{OUT}L_{OUT}

B

RHP3D2

LHP3D2

L_{IN}I²CSPi_V_{DD}R_{IN}

C

MONO_IN-

MONO_IN+

BYPASS

SCL

SDA

D

GND

RHP3D1

V_{DD}

ID_ENB

GND

E

LHP3D1

MONO-

V_{DD}

MONO+

I²CSPi_SEL