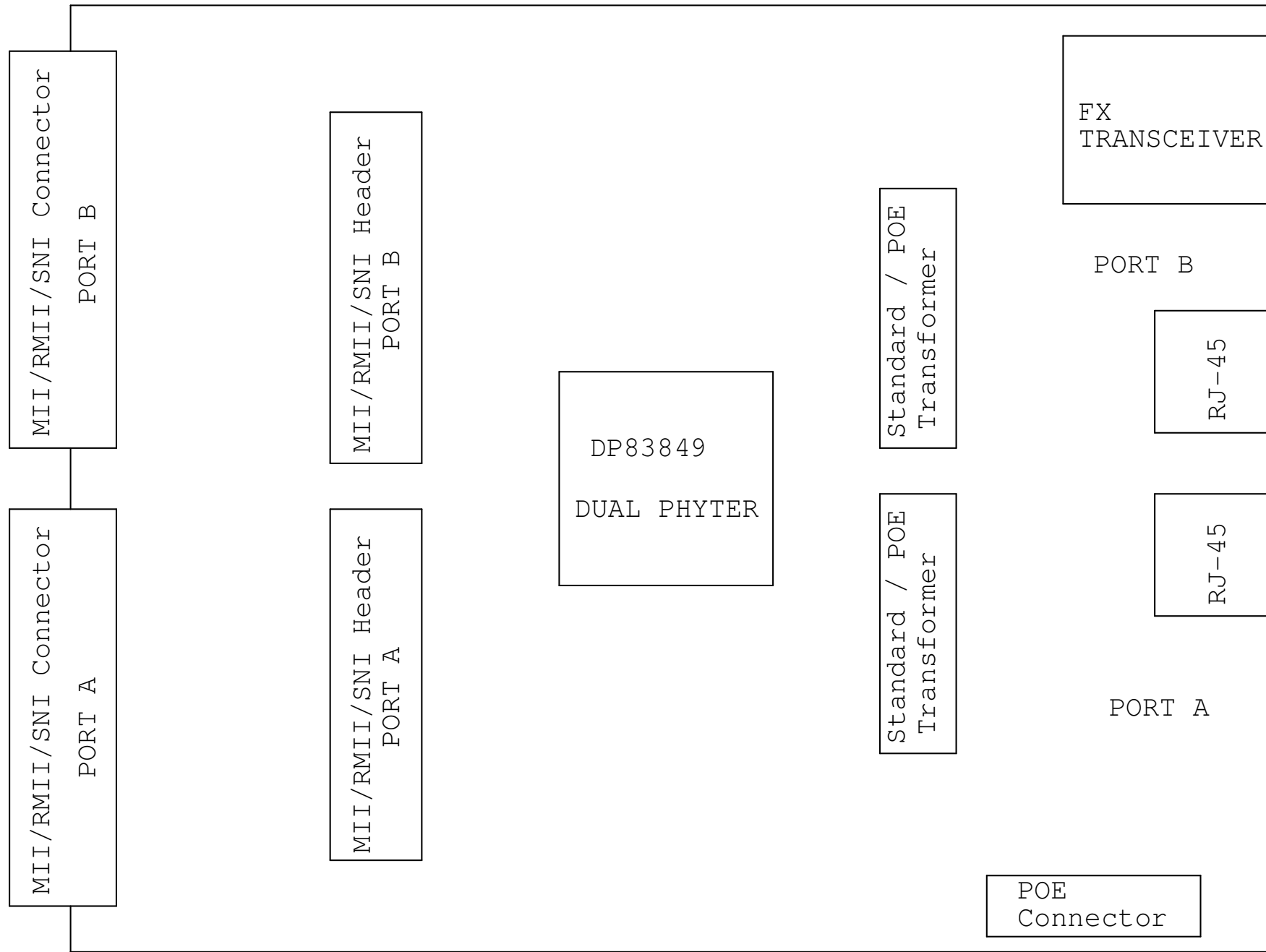


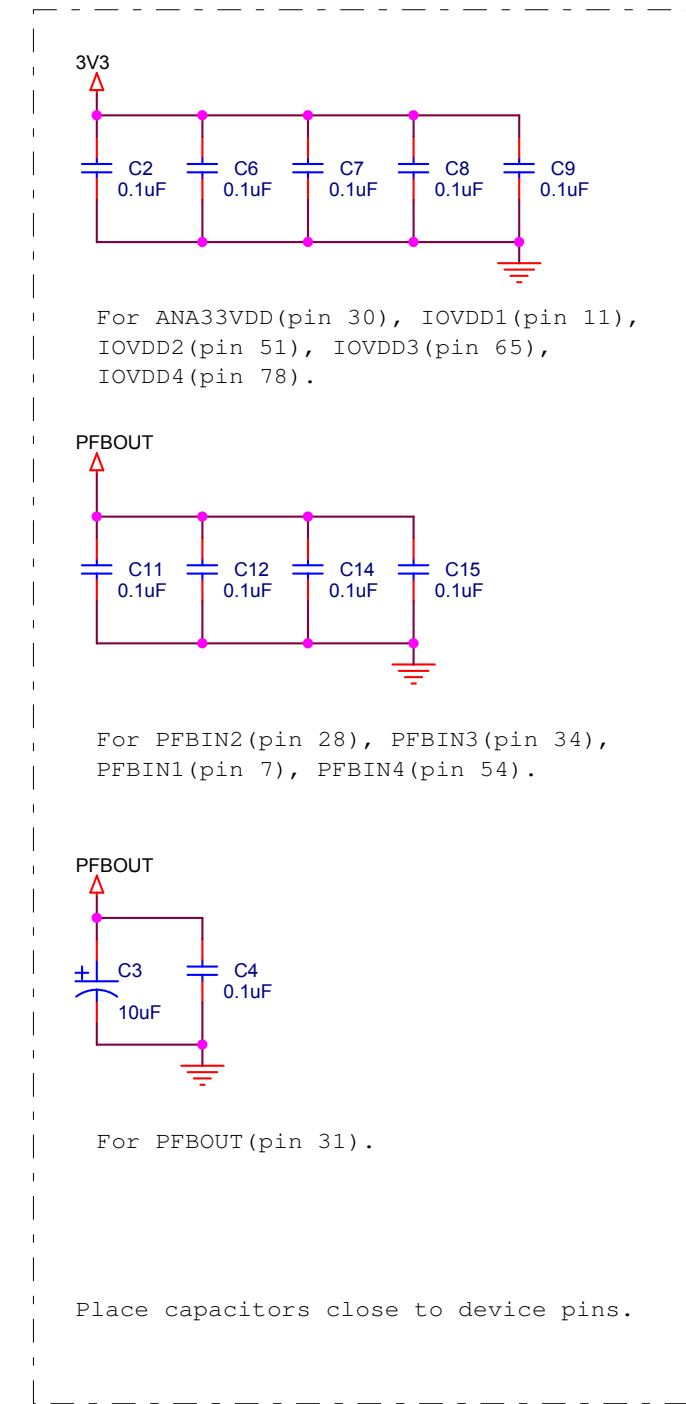
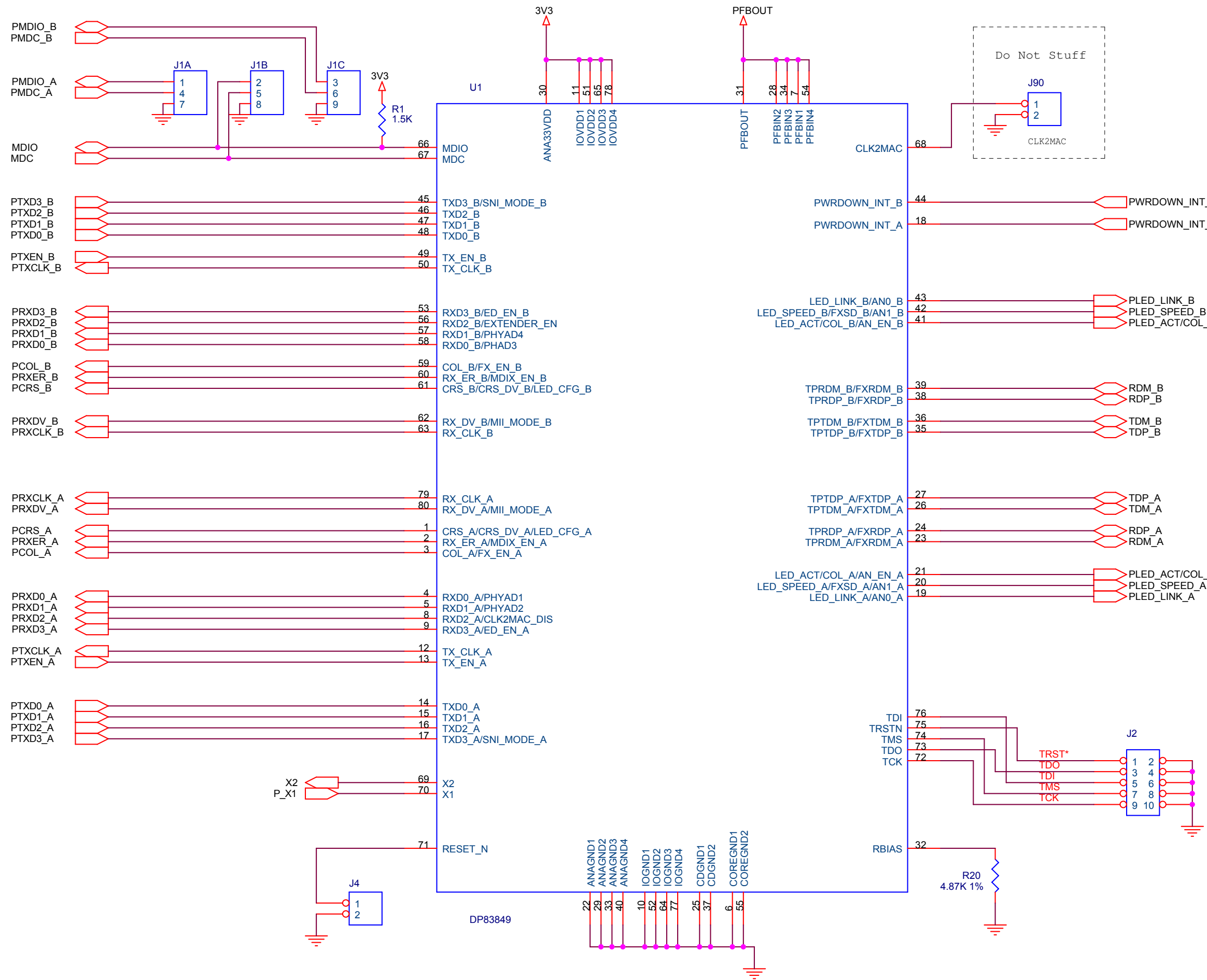
These schematics are provided for reference only. For any designs based on these schematics always contact National Semiconductor Corporation BEFORE initiating PCB manufacturing and ask for your design to be reviewed. Copyright (c) 2005 National Semiconductor Corporation. All Rights Reserved. Unpublished rights reserved under the copyright laws of the United States of America, other countries and international treaties. These schematics are provided without fee. Permission to use, copy, store, modify, disclose, transmit or distribute the schematics is granted, provided that this copyright notice must appear in any copy, modification, disclosure, transmission or distribution of the schematics. National Semiconductor Corporation retains all ownership, copyright, trade secret and proprietary rights in the schematics. THESE SCHEMATICS HAVE BEEN PROVIDED "AS IS", WITHOUT EXPRESS OR IMPLIED WARRANTY INCLUDING, WITHOUT LIMITATION, IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR USE AND NON-INFRINGEMENT.

Revisions note:

- Rev 1.0: Initial release.
- Rev 1.1:
 - Replaced D2, D3, D4, D5, D6, D7 with different LEDs
 - Removed D1 and R62
 - Changed R231 to 100 ohms



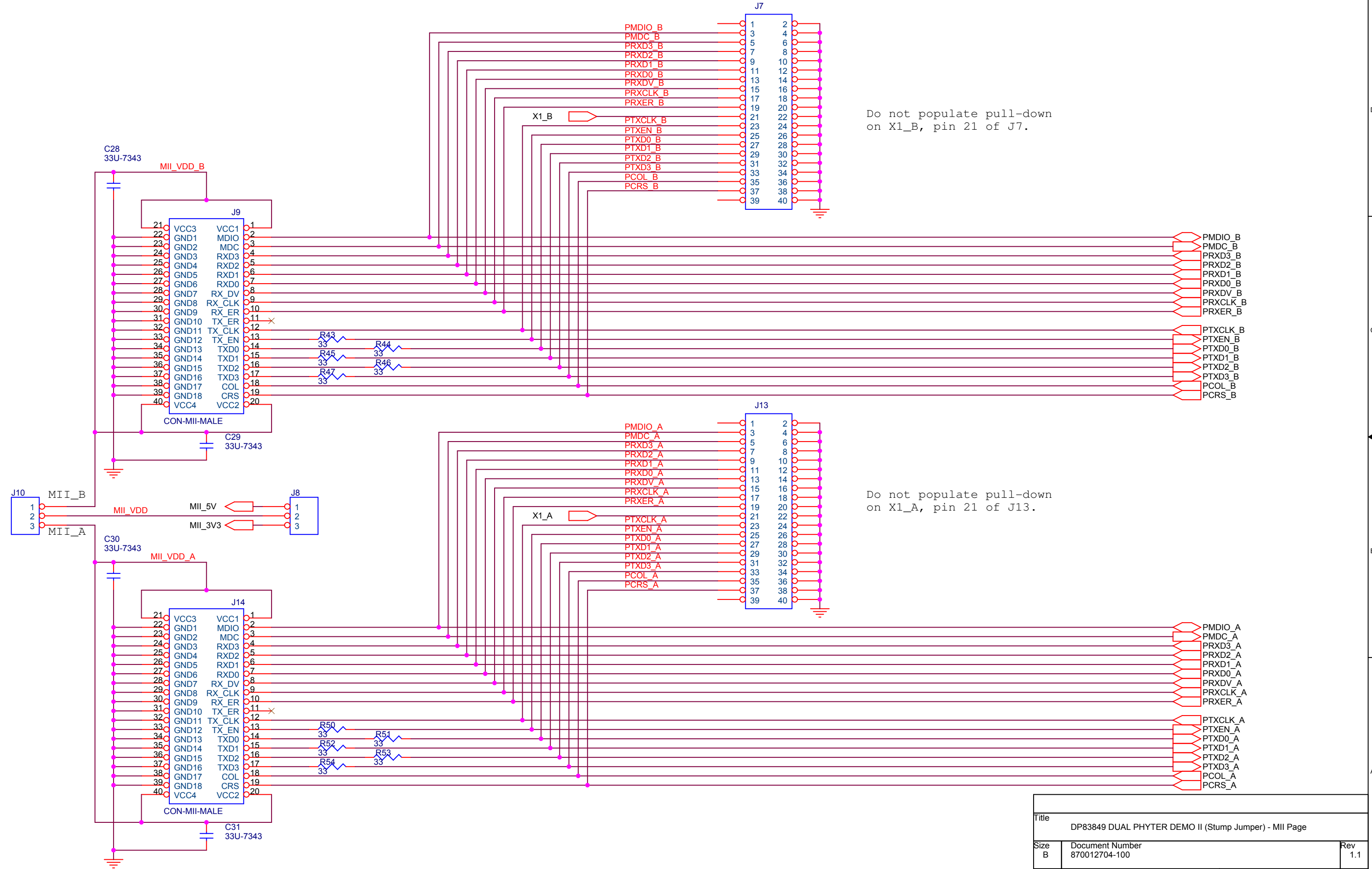
Title		
DP83849 DUAL PHYTER DEMO II (Stump Jumper) - Cover		
Size	Document Number	Rev
B	870012704-100	1.1
Date:	Thursday, December 15, 2005	Sheet 1 of 8



For PFBOUT (pin 31).

Place capacitors close to device pins.

Title DP83849 DUAL PHYTER DEMO II (Stump Jumper) - Ethernet PHY Page		
Size B	Document Number 870012704-100	Rev 1.1
Date: Thursday, December 15, 2005	Sheet 2	of 8

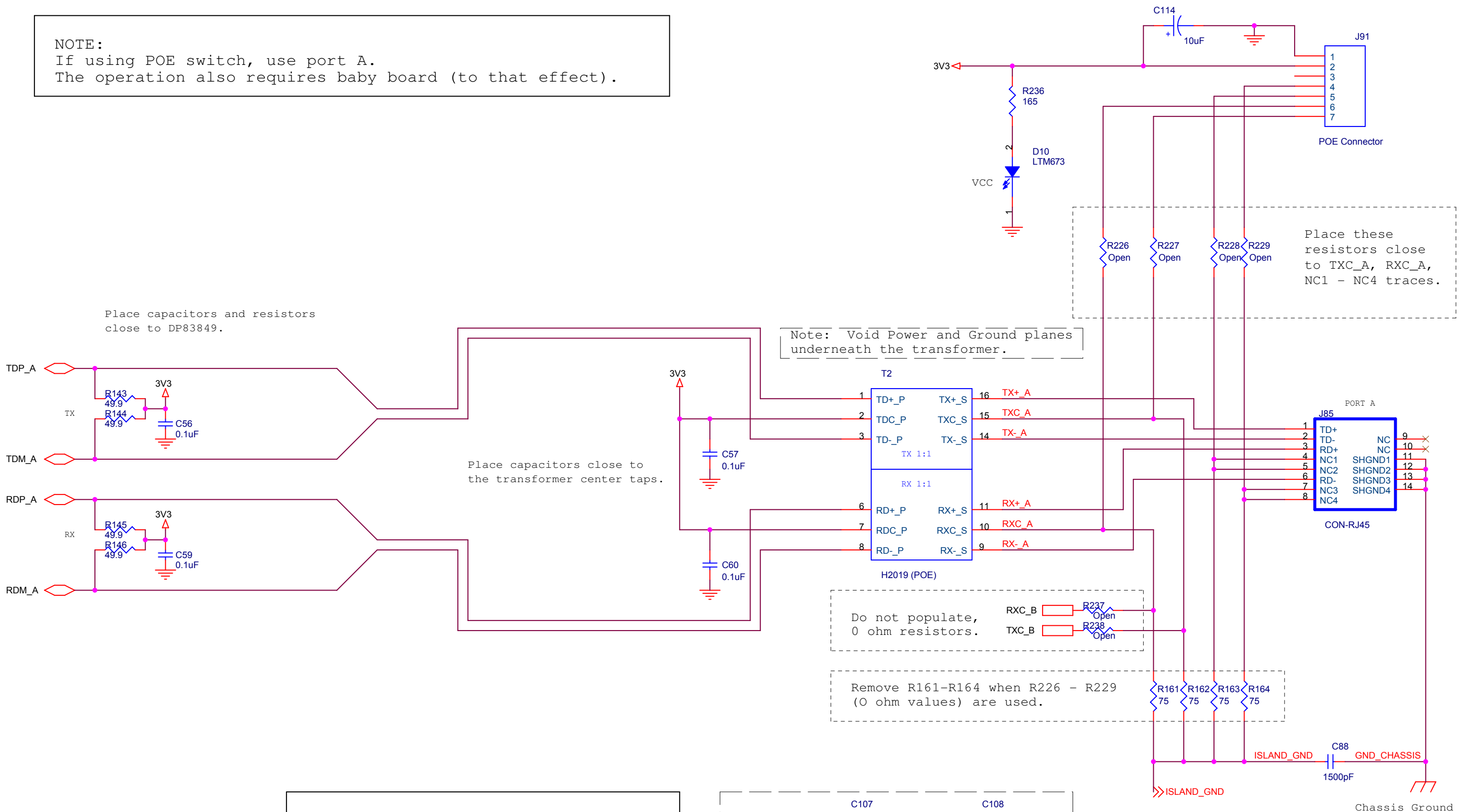


Do not populate pull-down on X1_B, pin 21 of J7.

Do not populate pull-down on X1_A, pin 21 of J13.

Title		
DP83849 DUAL PHYTER DEMO II (Stump Jumper) - MII Page		
Size	Document Number	Rev
B	870012704-100	1.1
Date:	Thursday, December 15, 2005	Sheet 3 of 8

NOTE:
 If using POE switch, use port A.
 The operation also requires baby board (to that effect).



Place capacitors and resistors close to DP83849.

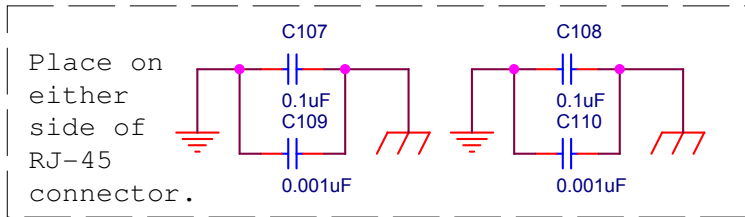
Place capacitors close to the transformer center taps.

Note: Void Power and Ground planes underneath the transformer.

Do not populate, 0 ohm resistors.

Remove R161-R164 when R226 - R229 (0 ohm values) are used.

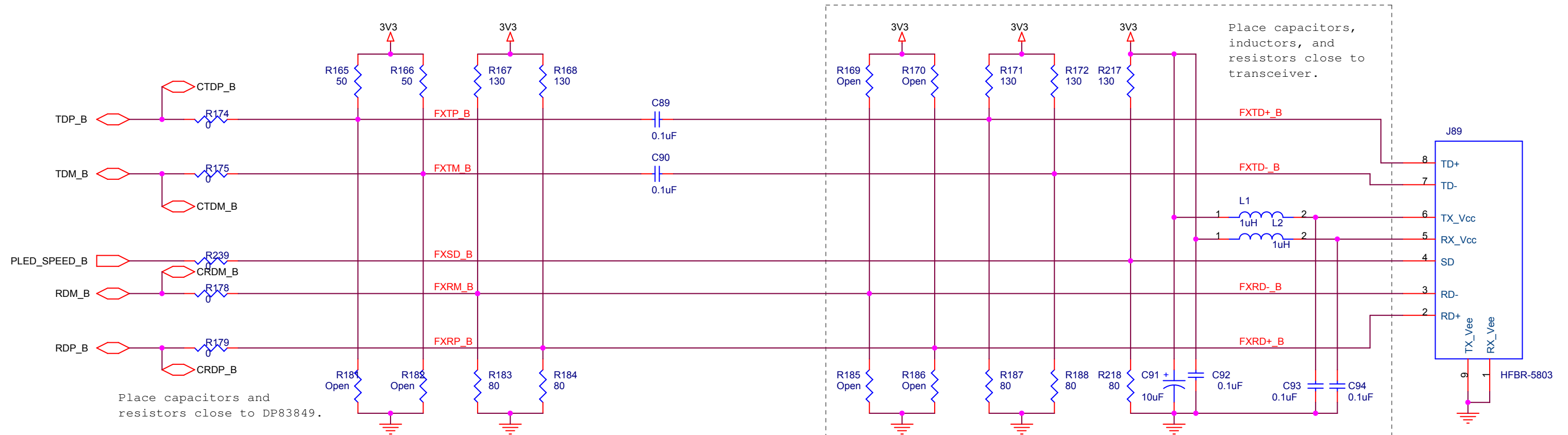
For magnetic:
 - Use H1102 for normal application
 - Use H2019 for POE application



Title		
DP83849 DUAL PHYTER DEMO II (Stump Jumper) - Port A TP Page		
Size	Document Number	Rev
B	870012704-100	1.1
Date:	Thursday, December 15, 2005	Sheet 4 of 8

NOTE:

- For fiber operation, populate all items on this page.
- For copper operation, do not populate all items on this page.



Place capacitors and resistors close to DP83849.

Place capacitors, inductors, and resistors close to transceiver.

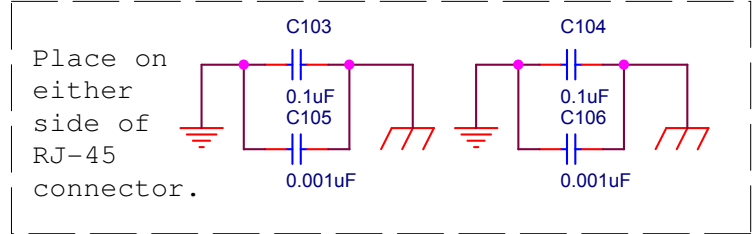
Important note:

- R174, R175, R178, R179, R239 are to be routed on bottom.

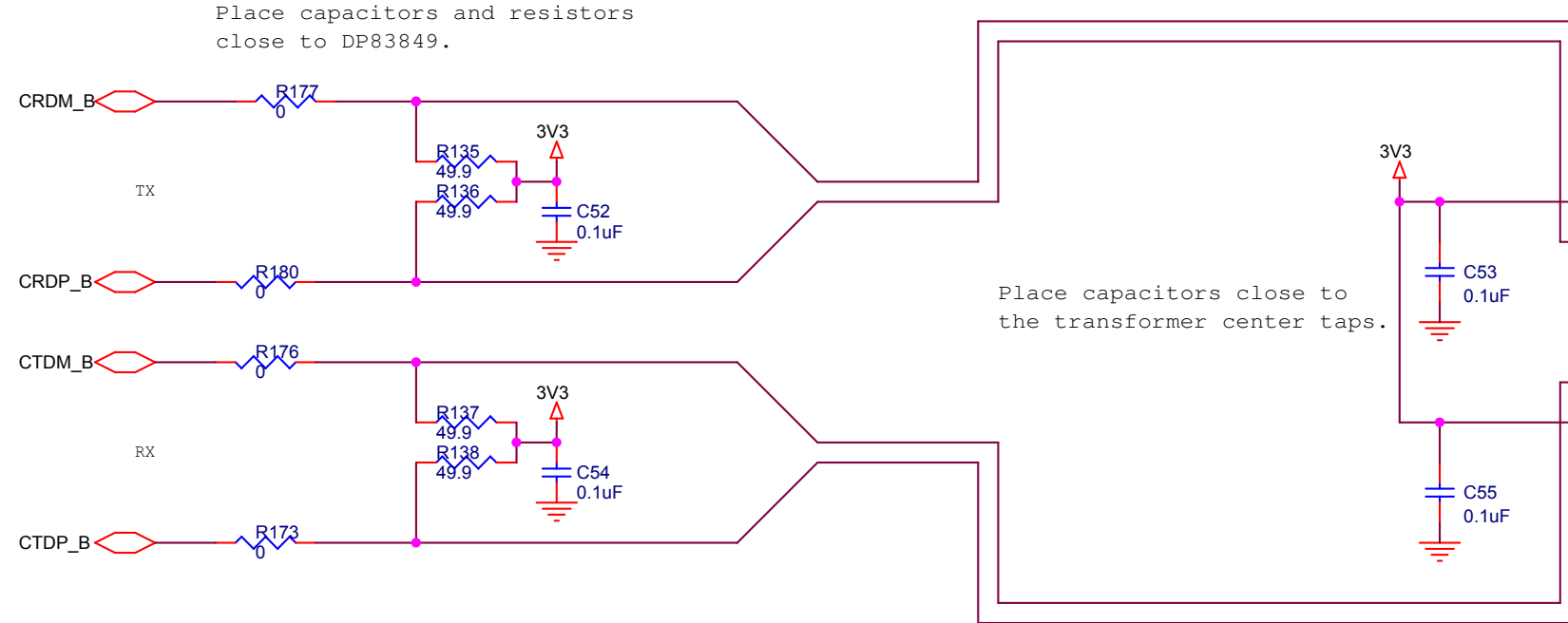
Title		
DP83849 DUAL PHYTER DEMO II (Stump Jumper) - Port B FX Page		
Size	Document Number	Rev
B	870012704-100	1.1
Date:	Thursday, December 15, 2005	Sheet 5 of 8

NOTE:

- For copper operation, populate all items on this page.
- For fiber operation, do not populate all items on this page.



Place capacitors and resistors close to DP83849.

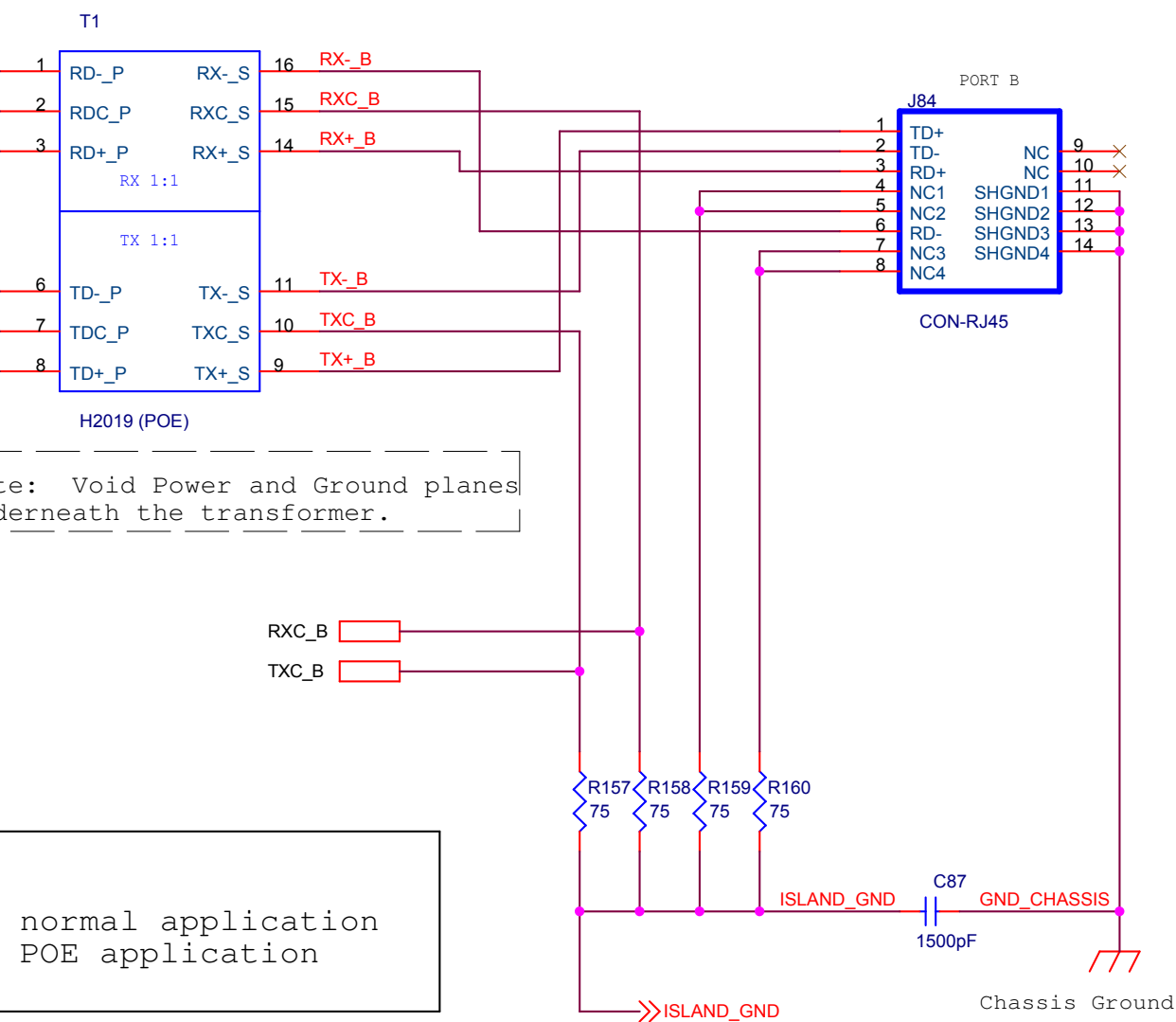


Place capacitors close to the transformer center taps.

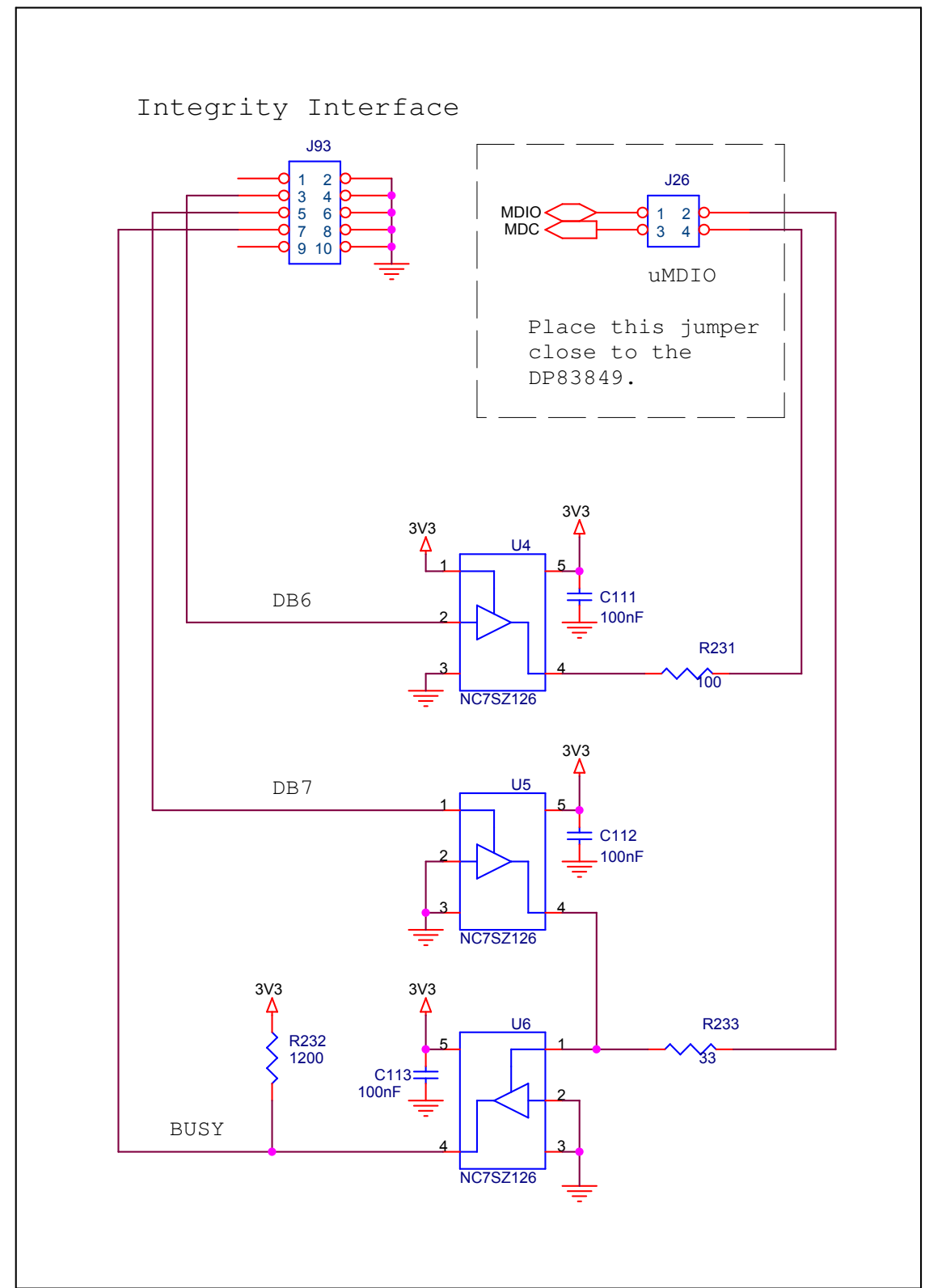
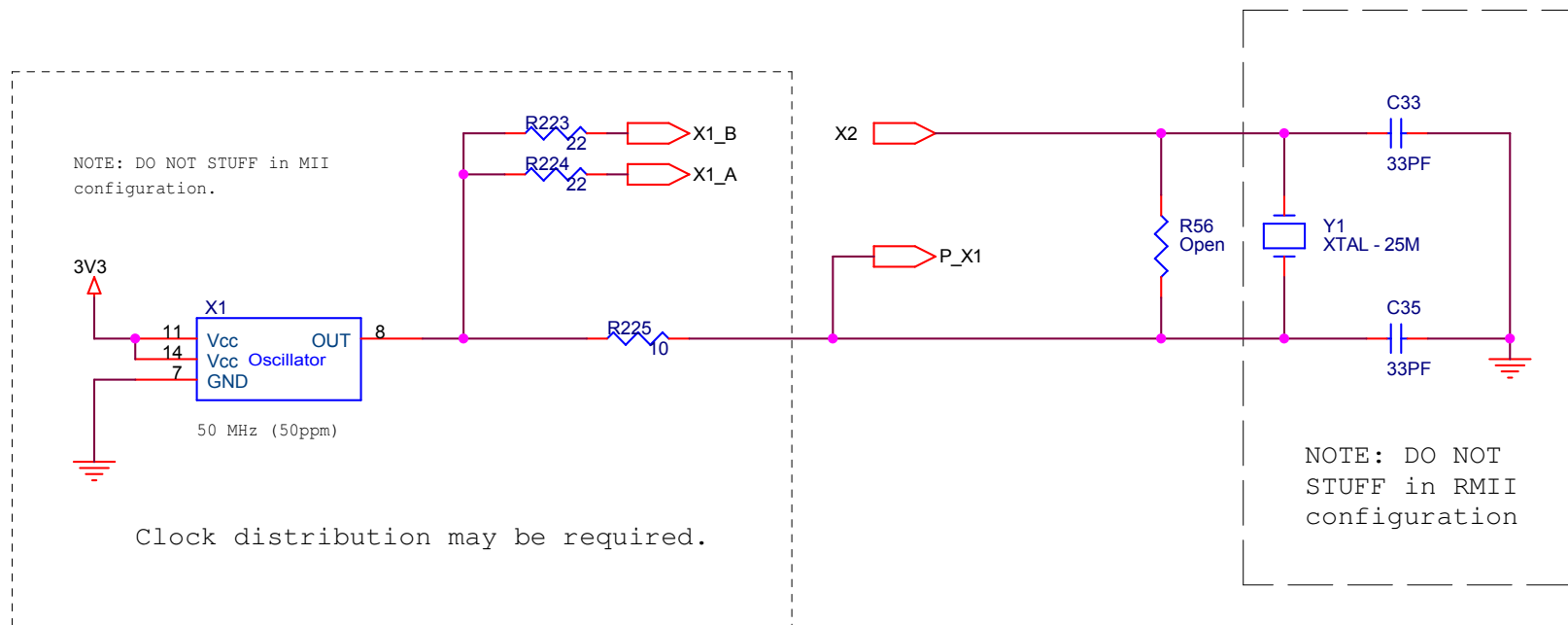
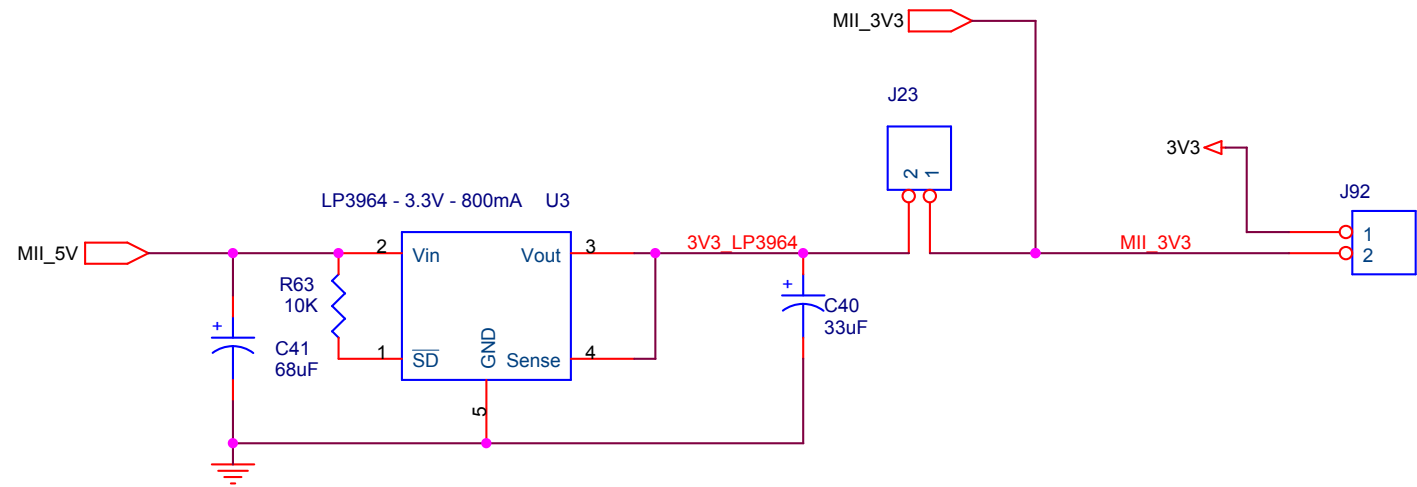
Note: Void Power and Ground planes underneath the transformer.

Important note:
- R173, R176, R177, R180 are to be routed on top.

For magnetic:
- Use H1102 for normal application
- Use H2019 for POE application



Title		
DP83849 DUAL PHYTER DEMO II (Stump Jumper) - Port B TP Page		
Size	Document Number	Rev
B	870012704-100	1.1
Date:	Thursday, December 15, 2005	Sheet 6 of 8



Title DP83849 DUAL PHYTER DEMO II (Stump Jumper) - Power, Clock, uMDIO Page		
Size B	Document Number 870012704-100	Rev 1.1
Date: Thursday, December 15, 2005	Sheet 7	of 8

