

Frequently Asked Questions

1. How do I buy SDXILEVK?

- **North America:** The SDXILEVK board can be purchased from Avnet. The Avnet part number for the board is AES-EXP-SDI-G. The Avnet product site can be accessed by following this [link](#).
- **Europe:** The SDXILEVK board can be purchased from EBV (<http://www.ebv.com>). The EBV part number for the board is SDXILEVK.
- **Japan:** The SDXILEVK board can be purchased from Avnet US site by following this [link](#). The Avnet part number for the board is AES-EXP-SDI-G.
- **Asia Pacific:** The SDXILEVK board can be purchased from Avnet US site by following this [link](#). The Avnet part number for the board is AES-EXP-SDI-G.

Distributors in Europe, Japan & Asia Pacific who do not wish to go through Avnet can purchase the board can purchase the board directly through National.

2. Does SDXILEVK (AES-EXP-SDI-G) come with the Spartan-3A development kit?

The daughter card does not come with the Spartan 3A development kit. It must be purchased separately.

- **North America:** The Spartan-3A development kit can be [purchased](#) from any of Xilinx distributors.
- **Europe:** The Spartan-3A development kit can be purchased from Silica (<http://www.silica.com/>).
- **Japan:** Japanese customers can order part number HW-SD1800A-DSP-SB-UNI-G-J through [local Japan distributor](#).
- **Asia Pacific:** The Spartan-3A development kit can be purchased from any of Xilinx distributors

3. Is the FPGA source code included in the SDXILEVK box? If not, how do I get access to the source code?

The FPGA IP source code is not included in the SDXILEVK box. For customers who purchased the board from Avnet, the source code for the FPGA can be downloaded (for free) from Avnet's website. To get the source code, customers need to do the following steps:

- Go to Avnet's EXP-SDI [website](#)
- Purchase the SDXILEVK (AES-EXP-SDI-G) board. If you purchased the daughter card through another distributor, have your P.O number ready.
- Follow the link "Support Files & Downloads" on Avnet's EXP-SDI website.
- The customer will be asked to register at Avnet's website. Follow registration procedures.

- Login with your registered user ID & password.
- Follow the link “EXP 3 Gbps SDI Module - IP Release Document”. Print and fill the IP request form. You will need your P.O. number to complete the form. Mail the form to the contacts listed in the form.
- Avnet will mail the IP reference design to you.

Access to the Xilinx FPGA firmware and source code for the EXP 3G SDI Module reference design is limited to customers who have purchased the SDXILEVK daughter card.

For customers/distributors who have purchased the board directly from National, the source code can be downloaded from National’s FTP server.

- The customer has to download the SLA (Software License Agreement). The form can be downloaded from National’s [website](#).
- The customer has to fill and sign the form.
- The completed form can be scanned and sent as e-mail or faxed to the local National representative. The customer also needs to provide their mailing address as a part of the SLA.
- Once National receives the SLA, it will be processed. The customer will receive a login ID and password to National’s FTP server that hosts the FPGA code.
- The customer can use this login to download the FPGA IP source code.

4. How do I get access to the source code for Spartan 3A FPGA IP? Do I need a SLA?

The source code for the FPGA can be downloaded from Avnet by registering at Avnet’s site as detailed above. No SLA is needed.

5. What formats are the source code available in?

The source code is available in both Verilog and VHDL formats.

6. Where do I locate information about the FPGA IP? Where is the FPGA IP description document located?

The Firmware description document is located in Xilinx\DOCS directory (after uncompressing the Zip file). The document provides a detailed description of the various modules and modes supported by the FPGA IP.

7. What are the formats supported the FPGA IP?

	Format	Specification
SD	576i25	SMPTE 259M /C
	486i29	SMPTE 259M /C
HD	720p23	SMPTE 292M
	720p24	SMPTE 292M
	720p29	SMPTE 292M

	720p30	SMPTE 292M
	720p50	SMPTE 296M
	720p59	SMPTE 296M
	720p60	SMPTE 296M
	1080sf23	SMPTE 274M + RP211
	1080sf24	SMPTE 274M + RP211
	1080i25	SMPTE 274M
	1080i29	SMPTE 274M
	1080i30	SMPTE 274M
	1080p23	SMPTE 274M
	1080p24	SMPTE 274M
	1080p25	SMPTE 274M
	1080p29	SMPTE 274M
	1080p30	SMPTE 274M
3G	1080p50	SMPTE 424M
	1080p59	SMPTE 424M
	1080p60	SMPTE 424M

8. What are the Dual link formats supported?

	Format	Specification
Dual Link	1080p50	SMPTE 372M + 425M
	1080p59	SMPTE 372M + 425M
	1080p60	SMPTE 372M + 425M

9. What are the audio formats supported by this FPGA IP?

Currently the FPGA source code supports 4 simultaneous channels of audio embedding/de-embedding. The 4 channels can be chosen from one of 4 groups. The audio format supported is I2S. Audio support for AES formats will be added shortly.

10. Who do I contact for support?

- **North America:** Customer support for the FPGA IP is provided by Avnet. Contact your local Avnet representative.
- **Europe:** Customer support for the FPGA IP is provided by Silica. Contact your local Silica representative.
- **Japan:** Customer support for the FPGA IP is provided by National/Avnet US.
- **Asia Pacific:** Customer support for the FPGA IP is provided by National/Avnet US.

The table below summarizes the Hardware vendors and the Customer support for FPGA IP.

Region	SDXILEVK	Xilinx Spartan-	FPGA IP	FPGA IP
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	(AES-EXP-SDI-G) Daughter card	3A Development board	source code	support
North America	Avnet	Avnet	Avnet.com w/ site registration or National with SLA	Avnet
Europe	EBV	Silica	Avnet.com w/ site registration or National with SLA	Silica
Japan	Avnet	Avnet	Avnet.com w/ site registration or National with SLA	Avnet US/National
Asia Pacific	Avnet	Avnet	Avnet.com w/ site registration or National with SLA	Avnet US/National