

DP83847

Special Connections:

- Pin 42 - Output of Reference Bypass Regulator. Required two caps close to pin and coupled to ground: 10uF (tantalum preferred) and .1uF (regular)
- Pin 3 - RBIAS. Need a 10k (1%) pull down resistor.
- Pin 14, 28, 56 (VDD pins) - Each need .1uF (close to pin) to ground
- Pin 44 – Internal Voltage Regulator Enable. Provide access to this pin with 1uF pull up capacitor to 3.3V, but do not populate this component.
- Recommend connection for pins 57-65, based on our reference design:
 - Pin 57 (VDD bar) connects to pin 14
 - Pin 59 (VDD bar) connects to pin 56
 - Pin 63 (VDD bar) connects to pin 28
 - Leave pin 61 (VDD bar) unconnected.
 - Pin 58, 60, 62, 64 (GND bars) connect to pin 65 (Center Ground)Soldered all pins 57-64. Do not have vias connect to these pins.
- Latest Apps Notes 1187 on soldering 56LLP package at the following link:
<http://www.nsc.com/packaging/llp/>
- Check reference board layout for board design