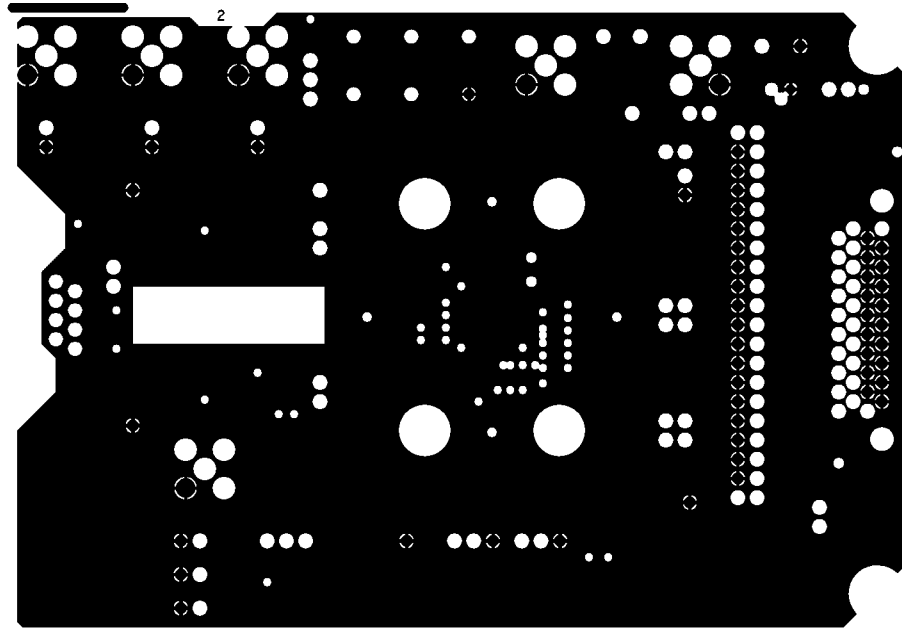

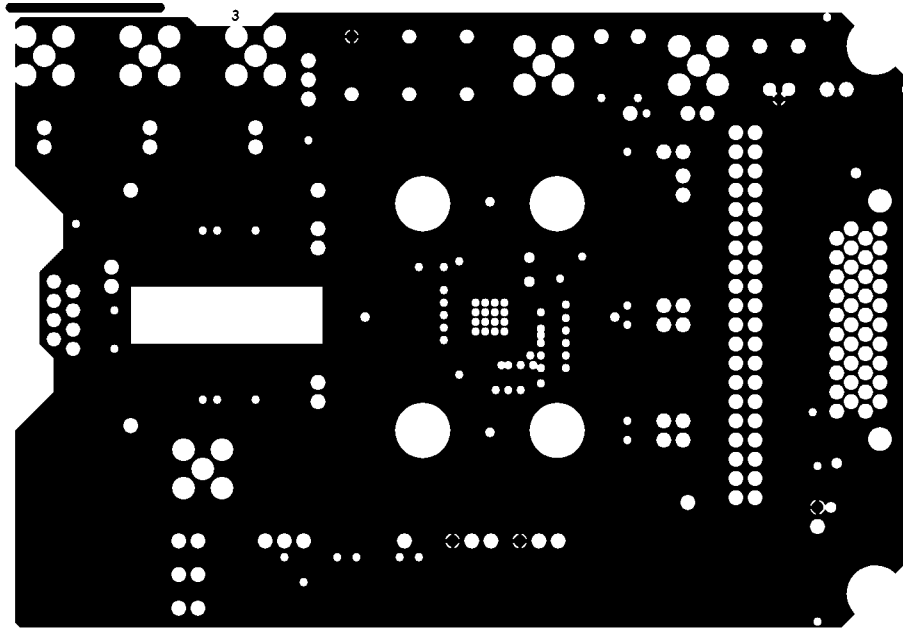

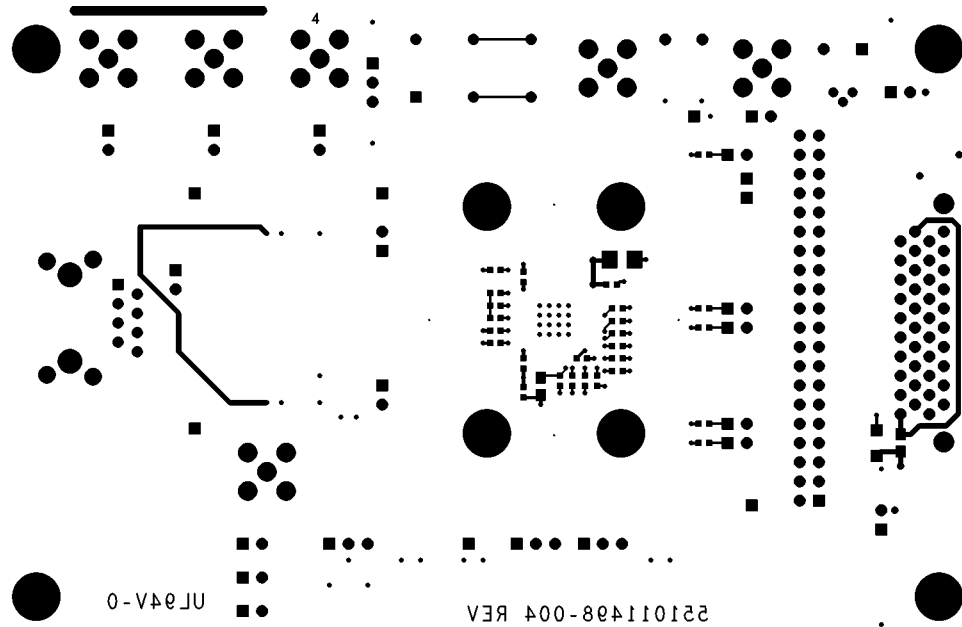
 <b>National Semiconductor™</b> NETWORKS DIVISION	ALL ARTWORK VIEWED FROM TOP SIDE	DATE: SEPTEMBER 7, 2001	
	BOARD NAME: DSPHYTER II MAU DEMO 2	BOARD NO: 880011498-004	REV A
	LAYER NAME: TOP LAYER 1	LAYER NO: 1	SHEET 1 of 8



 <b>National Semiconductor™</b> NETWORKS DIVISION	ALL ARTWORK VIEWED FROM TOP SIDE	DATE: SEPTEMBER 7, 2001	
	BOARD NAME: DSPHYTER II MAU DEMO 2	BOARD NO: 880011498-004	REV A
	LAYER NAME: LAYER 2 GND	LAYER NO: 2	SHEET 2 of 8




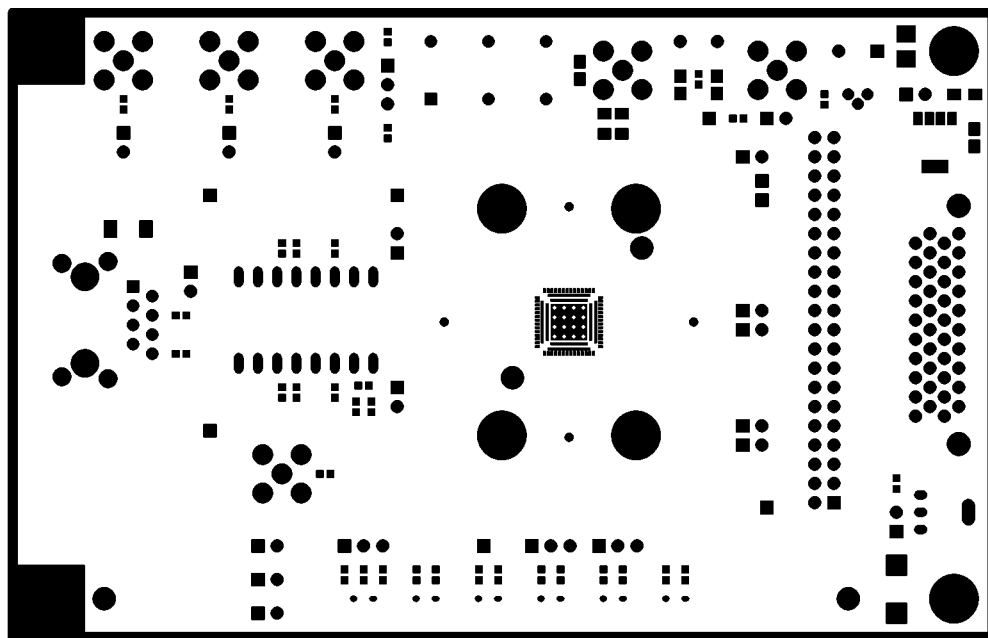
 <b>National Semiconductor™</b> NETWORKS DIVISION	ALL ARTWORK VIEWED FROM TOP SIDE	DATE: SEPTEMBER 7, 2001	
	BOARD NAME: DSPHYTER II MAU DEMO 2	BOARD NO: 880011498-004	REV A
	LAYER NAME: LAYER 3 3V3	LAYER NO: 3	SHEET 3 of 8




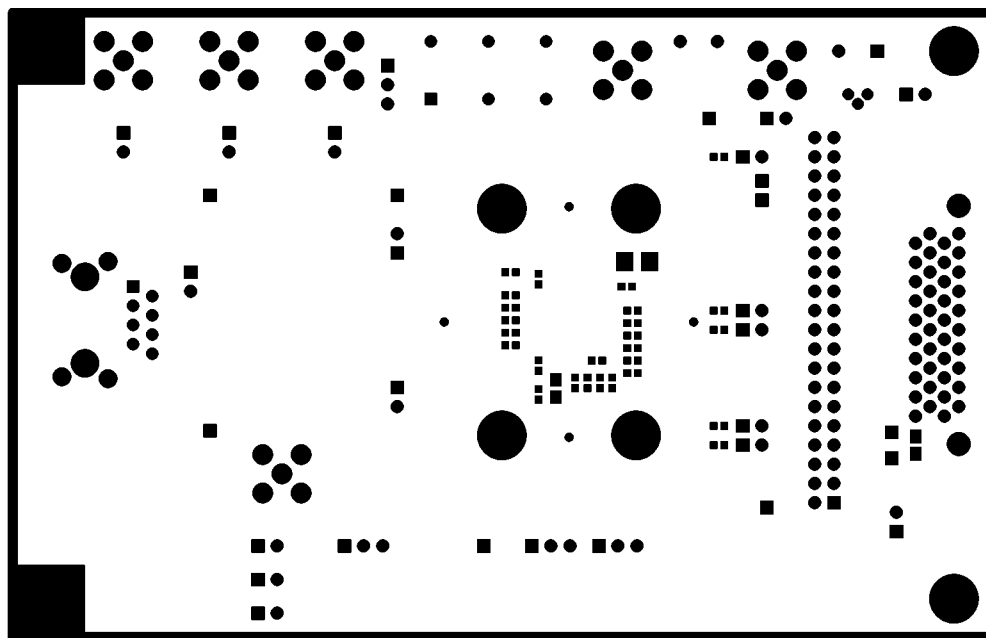
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VER 400-808-004 REV

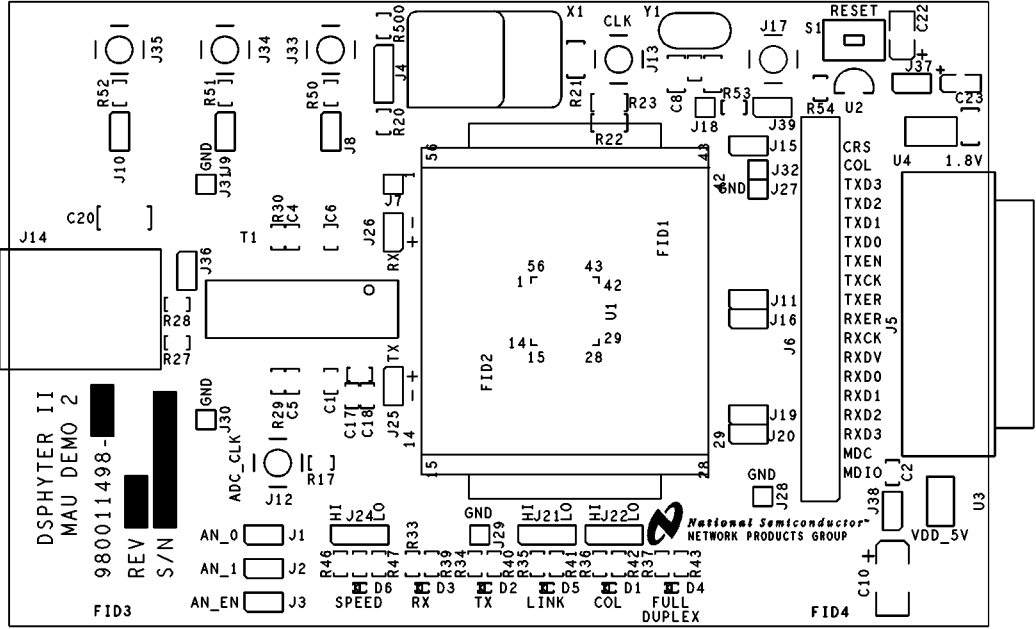
 <b>National Semiconductor™</b> NETWORKS DIVISION	ALL ARTWORK VIEWED FROM TOP SIDE	DATE: SEPTEMBER 7, 2001	
	BOARD NAME: DSPHYTER II MAU DEMO 2	BOARD NO: 880011498-004	REV A
	LAYER NAME: BOTTOM LAYER 4	LAYER NO: 4	SHEET 4 of 8




 <b>National Semiconductor™</b> NETWORKS DIVISION	ALL ARTWORK VIEWED FROM TOP SIDE	DATE: SEPTEMBER 7, 2001	
	BOARD NAME: DSPHYTER II MAU DEMO 2	BOARD NO: 880011498-004	REV A
	LAYER NAME: TOP SOLDER MASK	LAYER NO: 1	SHEET 5 of 8

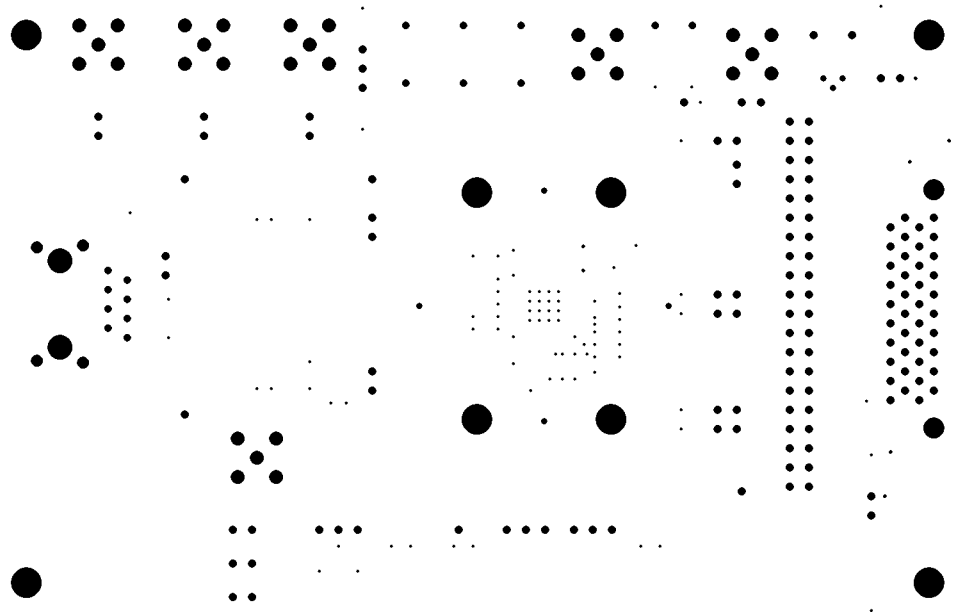


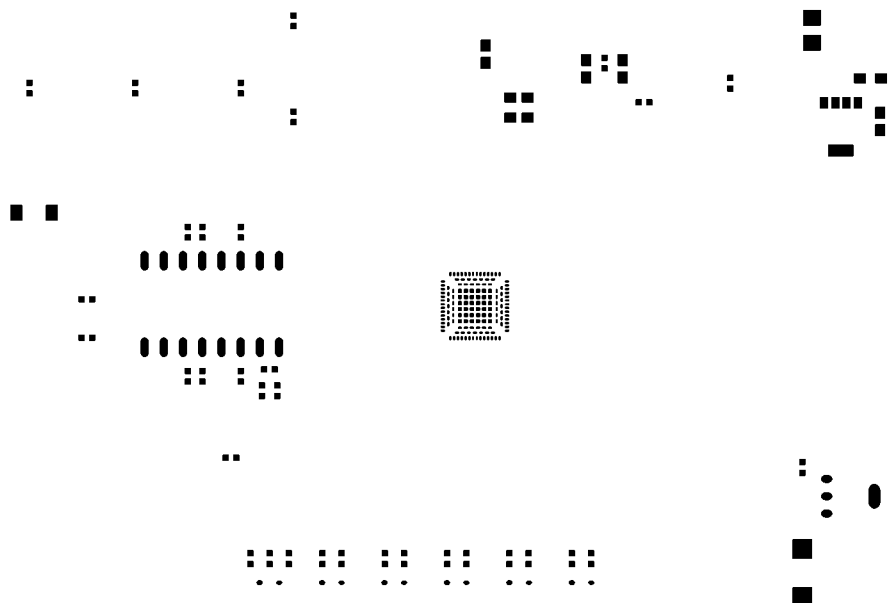
 <b>National Semiconductor™</b> NETWORKS DIVISION	ALL ARTWORK VIEWED FROM TOP SIDE	DATE: SEPTEMBER 7, 2001	
	BOARD NAME: DSPHYTER II MAU DEMO 2	BOARD NO: 880011498-004	REV A
	LAYER NAME: BOTTOM SOLDER MASK	LAYER NO: 4	SHEET 6 of 8




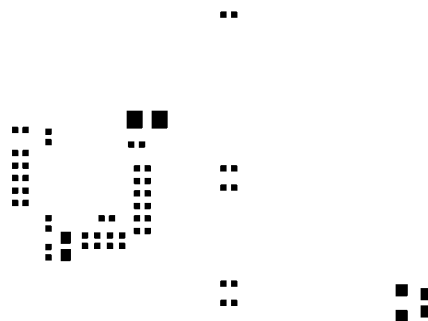
 <b>National Semiconductor™</b> NETWORKS DIVISION	ALL ARTWORK VIEWED FROM TOP SIDE		DATE: SEPTEMBER 7, 2001
	BOARD NAME: DSPHYTER II MAU DEMO 2		BOARD NO: 880011498-004 REV A
	LAYER NAME: TOP SILKSCREEN	LAYER NO: 1	SHEET 7 of 8








 <b>National Semiconductor™</b> NETWORKS DIVISION	ALL ARTWORK VIEWED FROM TOP SIDE	DATE: SEPTEMBER 7, 2001	
	BOARD NAME: DSPHYTER II MAU DEMO 2	BOARD NO: 881011498-004	REV A
	LAYER NAME: TOP PASTE	LAYER NO: 1	SHEET 1 of 2



 <b>National Semiconductor™</b> NETWORKS DIVISION	ALL ARTWORK VIEWED FROM TOP SIDE	DATE: SEPTEMBER 7, 2001	
	BOARD NAME: DSPHYTER II MAU DEMO 2	BOARD NO: 881011498-004	REV A
	LAYER NAME: BOTTOM PASTE	LAYER NO: 4	SHEET 2 of 2