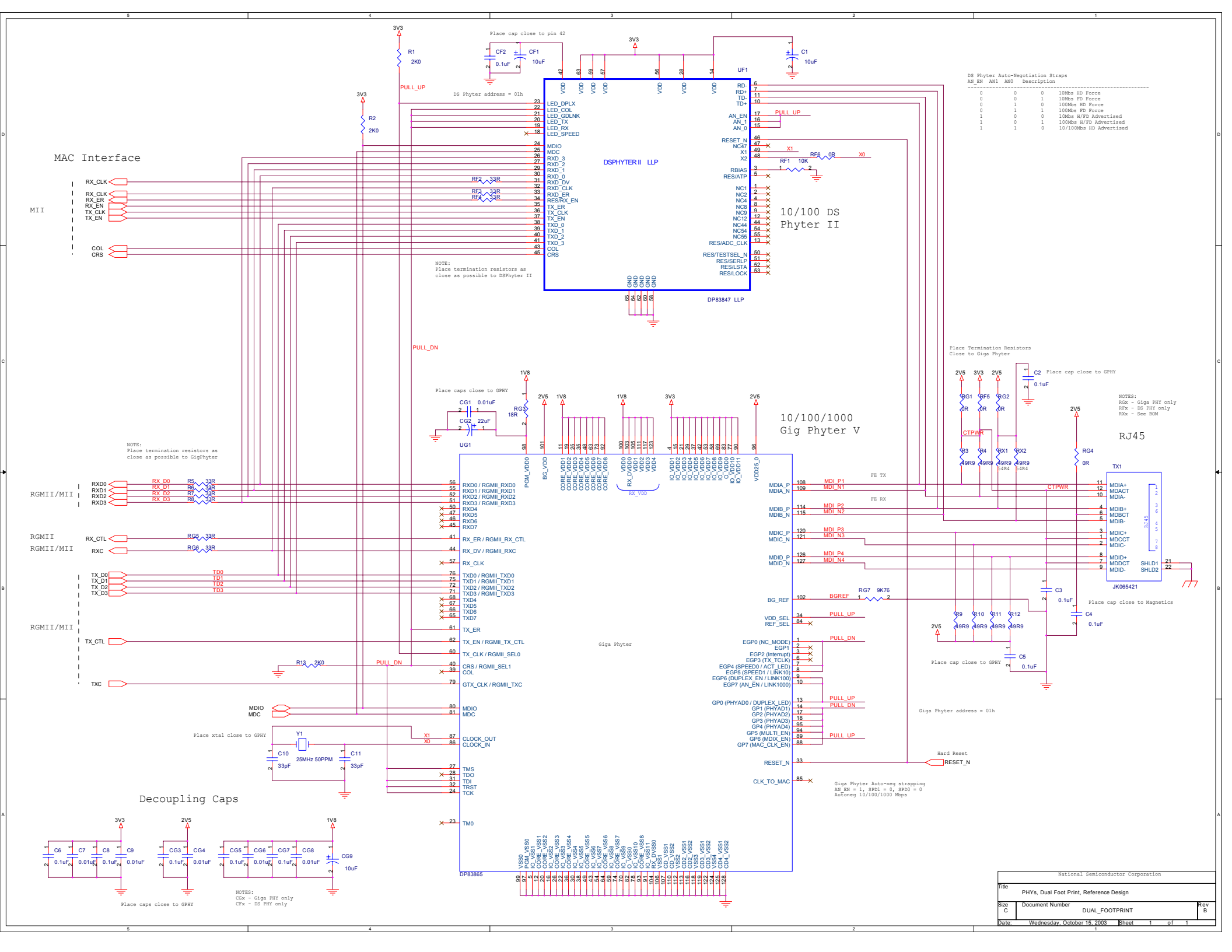
MAC Interface
MII
RX_CLK
RX_CLK
RX_ER
RX_EN
TX_CLK
TX_EN
COL
CRS
DSPHYTER II LLP
DP83847 LLP
10/100 DS
Phyter II
10/100/1000
Gig Phyter V
Giga Phyter
DP83865
RJ45
JK065421
RGMII/MII
RGMII
RGMII/MII
RGMII/MII
RXD0
RXD1
RXD2
RXD3
RX_CTL
RXC
TX_D0
TX_D1
TX_D2
TX_D3
TX_CTL
TXC
MDIO
MDC
Decoupling Caps
Place caps close to GPHY
Place cap close to pin 42
Place caps close to GPHY
Place Termination Resistors
Close to Giga Phyter
Place cap close to GPHY
Place cap close to Magnetics
Place xtal close to GPHY
Giga Phyter address = 01h
DS Phyter address = 01h
Hard Reset
RESET_N
PULL_UP
PULL_DN
DS Phyter Auto-Negotiation Straps
AN_EN AN1 AN0 Description
0 0 0 10Mbs HD Force
0 0 1 10Mbs FD Force
0 1 0 100Mbs HD Force
0 1 1 100Mbs FD Force
1 0 0 10Mbs H/FD Advertised
1 0 1 100Mbs H/FD Advertised
1 1 0 10/100Mbs HD Advertised
Giga Phyter Auto-neg strapping
AN_EN = 1, SPD1 = 0, SPD0 = 0
Autoneg 10/100/1000 Mbps
NOTE:
Place termination resistors as
close as possible to DSPhyter II
NOTE:
Place termination resistors as
close as possible to GigPhyter
NOTES:
RGx - Giga PHY only
RFx - DS PHY only
RXx - See BOM
NOTES:
CGx - Giga PHY only
CFx - DS PHY only
National Semiconductor Corporation
Title
PHYs, Dual Foot Print, Reference Design
Size C
Document Number
DUAL_FOOTPRINT
Rev B
Date: Wednesday, October 15, 2003
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