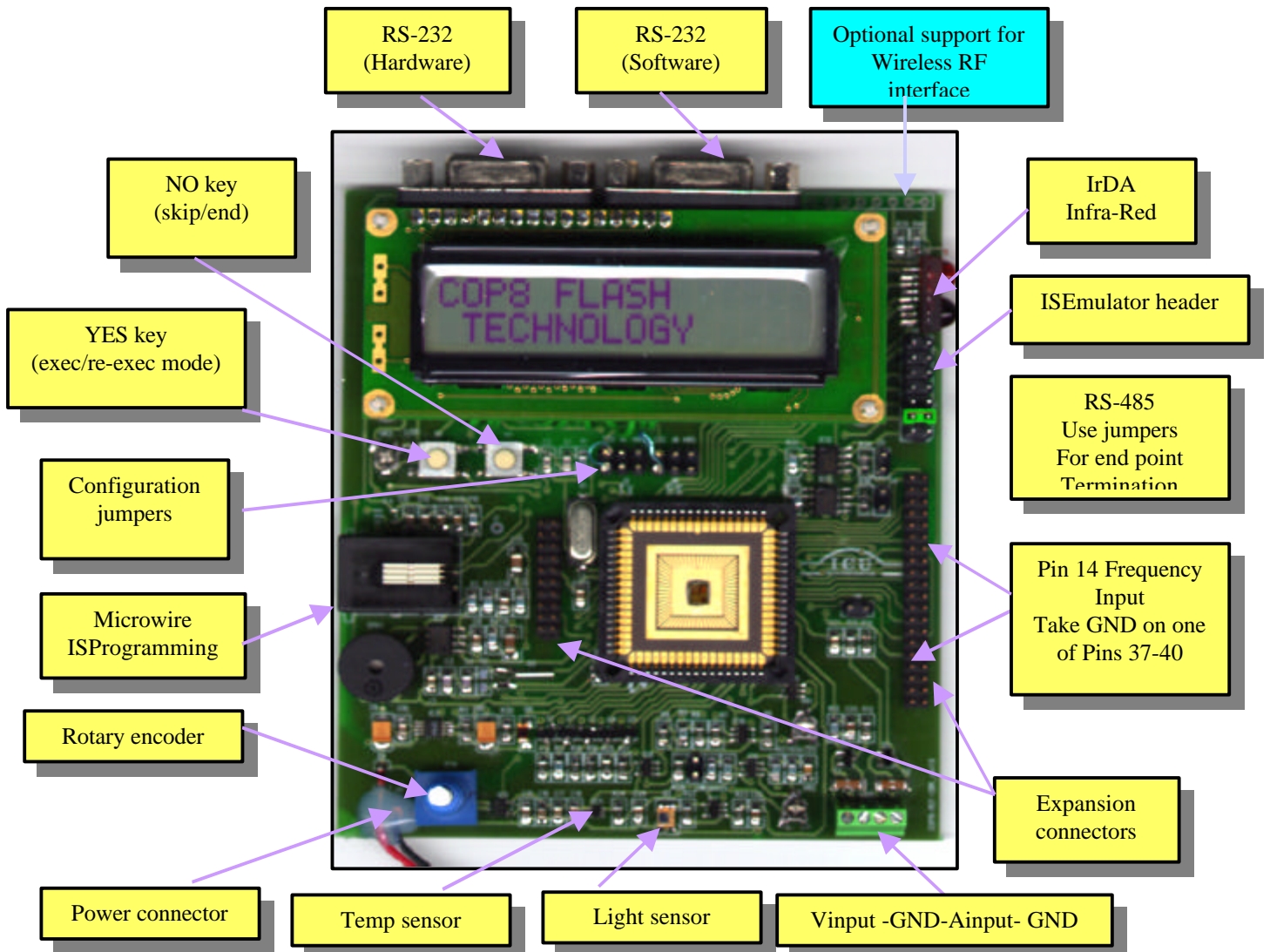


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6 Hardware Description

Here is a description of the main hardware ideas behind the COP8-REF-FL1 reference design. This picture illustrates functions supported by this references design. The schematics and bill of material are on the provided CD-ROM.



6.1 Description of the expansion connector

There is an expansion connector on the references design (J14). This connector has a standard spacing of 2 mm. In the schematics there is a description of the signals going to this connector. An overview of signals of the header a detailed description is done here. Note that the J14



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connector is divided into one 40 pin header and one 20 pin header and that the pin numbering on this is done in the same way as is on an integrated circuit.

Pin No.	Port	Pin No.	Port	Pin No.	Port	Pin No.	Port
1	N.U	40	GND				
2	N.U	39	GND				
3	N.U	38	GND				
4	N.U	37	GND				
5	N.U	36	+5V				
6	N.U	35	+5V	41	Ext. VCC	60	N.U
7	N.U	34	N.U	42	Ext. VCC	59	GND
8	N.U	33	E0	43	N.U	58	C7
9	N.U	32	E1	44	N.U	57	N.U
10	N.U	31	L3*	45	N.U	56	C2
11	D2	30	L2	46	GND	55	GND
12	D3	29	G5	47	N.U	54	N.U
13	G2	28	G6	48	N.U	53	N.U
14	G3	27	G4	49	N.U	52	N.U
15	A6	26	D0	50	N.U	51	N.U
16	A7	25	E2				
17	RS485	24	E3				
18	RS485	23	D4				
19	G0	22	D1				
20	D6	21	D5				

Explanations:

N.U short for not used

* Must use shorting block on (J16)

6.2 ISP-programming considerations

It is very important that the PC-parallel port cable to the RJ11 connector on the reference board is disconnected when resetting the board to get into ISP mode. To have the cable connected will not harm the reference design but the high signals on the parallel interface will prevent the COP8 flash from doing a correct reset.